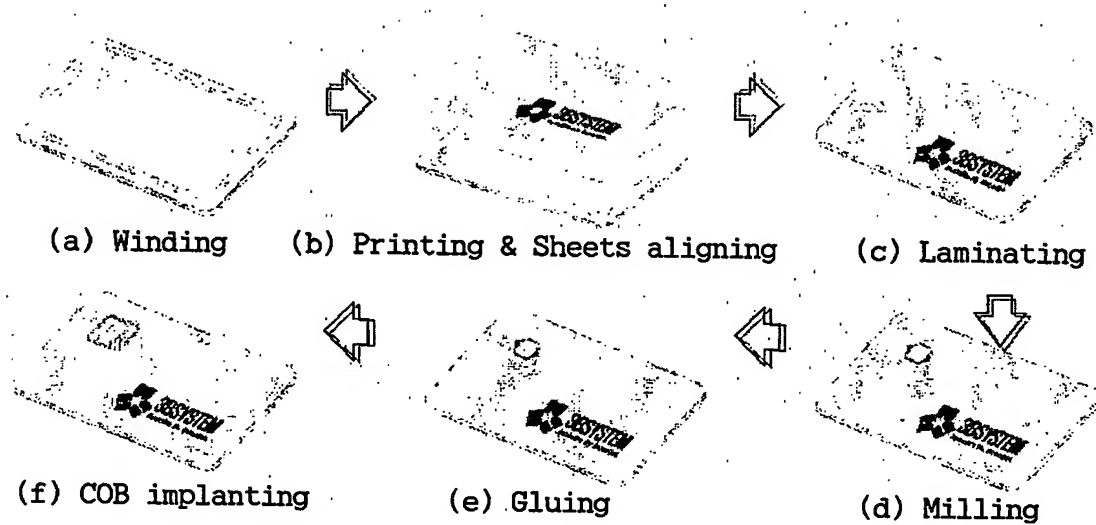


1/6

FIG. 1



2/6

FIG. 2

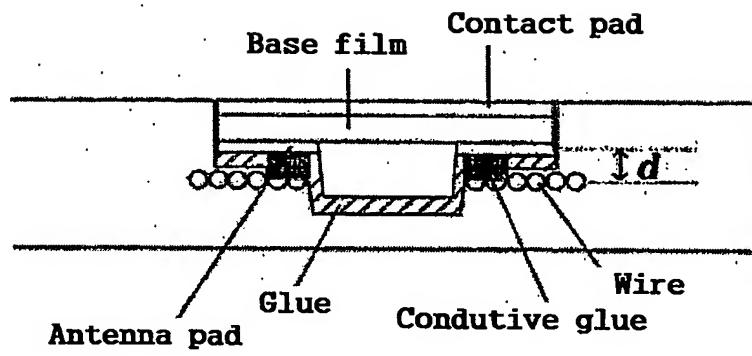
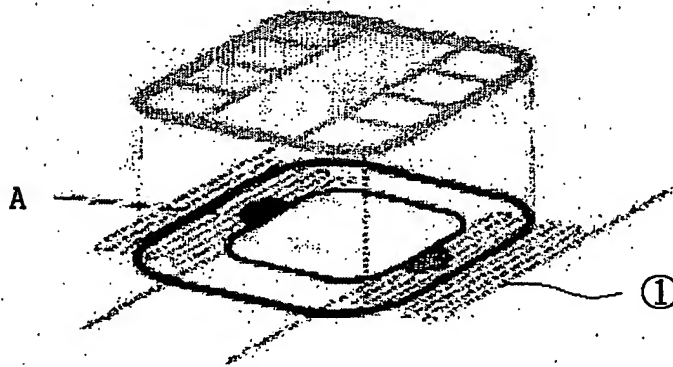


FIG. 3



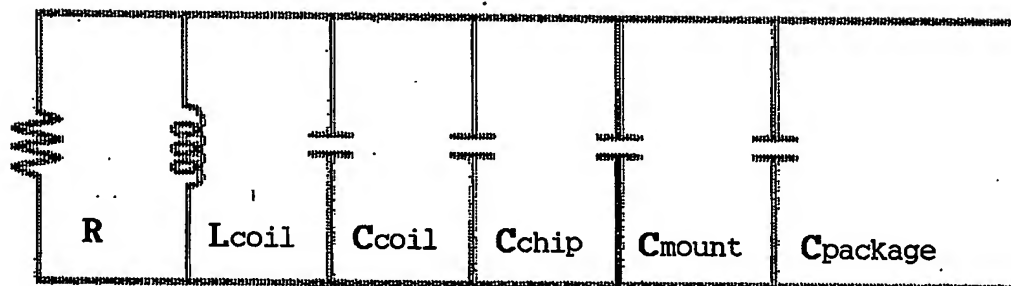
3/6

FIG. 4a

$$C_{mount} = \epsilon_o \epsilon_r \frac{A}{d} \quad (\text{Eq.1})$$

$$f_0 = \frac{1}{2\pi \sqrt{L_{coil} (C_{chip} + C_{coil} + C_{mount} + C_{package})}} \quad (\text{Eq.2})$$

FIG. 4b



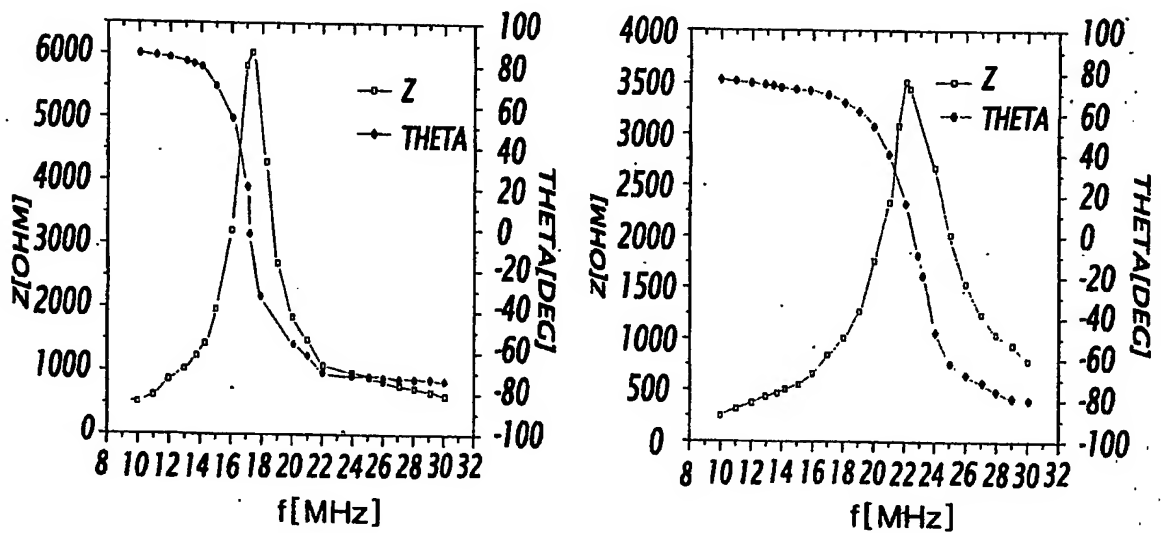
EQUIVALENT CIRCUIT OF THE CONVENTIONAL SMART CARD OF A
COMBINATION TYPE

FIG. 5a

MEASUREMENT VALUE(AVERAGE)	Δd	f_o	D
BEFORE TESTING	0	17.3MHz	80mm
AFTER TESTING	9 μ m	22.8MHz	62mm

TABLE FOR RESULT MEASURED BEFORE AND AFTER A BENDING TEST

FIG. 5b



(a) BEFORE TESTING
 (b) AFTER TESTING
 GRAPH OF IMPEDANCE-FREQUENCY CHARACTERISTICS IN
 THE CONVENTION SMART CARD OF A COMBINATION TYPE

5/6

FIG. 6

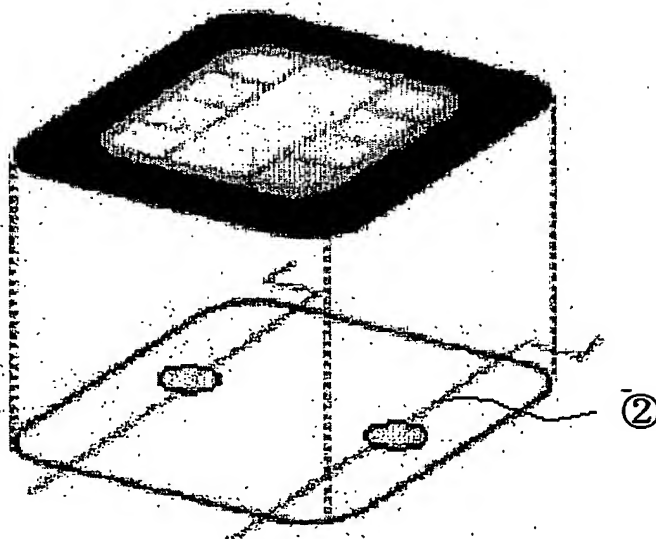
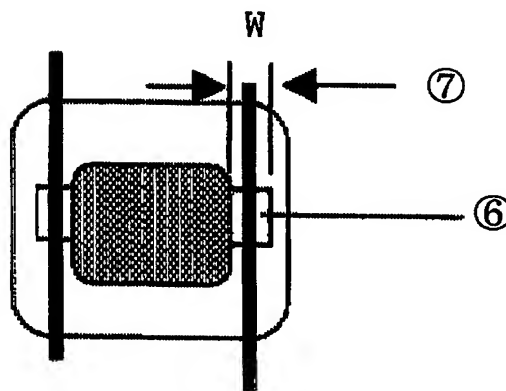


FIG. 7



ONE EMBODIMENT OF CONTACT PORTIONS BETWEEN A COB AND ENDS OF
AN ANTENNA IN A SMART CARD OF A COMBINATION TYPE ACCORDING TO
THE PRESENT INVENTION

6/6

FIG. 8

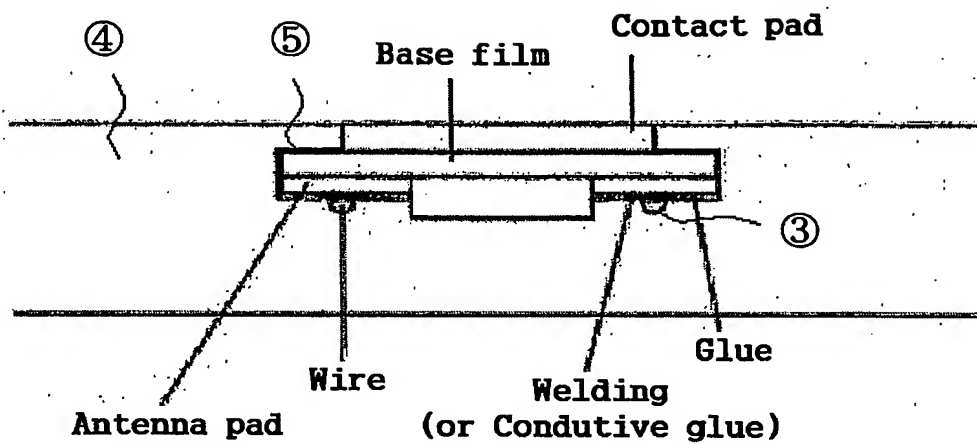


FIG. 9

